

MINI-COLLOQUIA ON COMPACT MODELLING TECHNIQUES FOR NANOSCALE DEVICES AND CIRCUIT

Mini-Colloquia on Compact Modelling Techniques for Nanoscale Devices and Circuit Analysis

Kimmi Makkar (Roll No: 15480), Aditya Chhilar (Roll No: 15449) and Rohan Yadav (Roll No: 15509), Student of IT VI Semester attended the Mini-Colloquia on "Compact Modelling Techniques for Nanoscale Devices and Circuit Analysis" at SP Jain Centre Auditorium, University of Delhi South Campus, Benito Juarez Road, New Delhi on March 13, 2015. It is organized by Organized by IEEE EDS-Delhi Chapter, New Delhi and sponsored by the IEEE Electron Devices Society.



Student at the event