Pool Campus Placement for VLSI Design and Embedded Systems Services

DCE/TPO/4711   **12 Sep 09**

There are number of openings in "VLSI Design and Embedded and Systems Services". Other details are as follows:

Batch	:	2009
Branch	:	ECE
Date	:	18 Sep 2009
Eligibility	:	B.Tech 70%, pro-active and self motivated, should have good communication skills. Candidate simulation exp. on HSPICE/SPECTRE/ELDO, deep understanding of fundamentals of VLSI.
Venue	:	RIMT-Institute of Engg. & Tech. (IET), Mandi, Gobindgarh

Selection Process   :
1. Written Test
2. Personal Interview

Note   :
1. Students are informed to carry all the mandatory documents like hard copy of resume, passport size photographs, photocopies of all semester DMC's and proof of identity.
2. All the students should come for Interview in formal dress.
3. Complete Biodata/CV of eligible students should be forwarded at **tporimt@yahoo.com** and a cc to **tpo@dronacharya.info** before Tuesday,
4. Hall tickets will be issued to short listed CV's and will be called for appearing in recruiting process.
5. Eligible students list attached.